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** CONTINUING DATA *****

This application is a REI of 08/311,021 09/22/1994 PAT 5,637,913
 which is a DIV of 08/038,684 03/29/1993 PAT 5,378,656

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** FOREIGN APPLICATIONS *****

JAPAN 4-320098 11/30/1992

JAPAN 4-071116 03/27/1992

nbe see in case 08/038 684

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 12/17/2001

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Verified and Acknowledged	JAPAN	21	27 13	8 4
Allowance <i>Examiner's Signature</i> <i>nbe</i> Initials				

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TITLE

LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME, AND METHOD OF
 AND PROCESS FOR FABRICATING THE TWO

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